

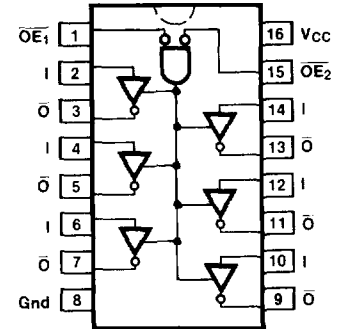
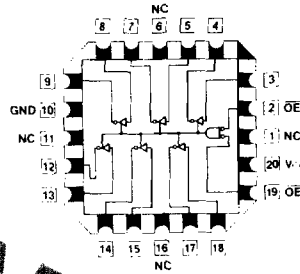
54F/74F366 • 54F/74F368

Connection Diagrams

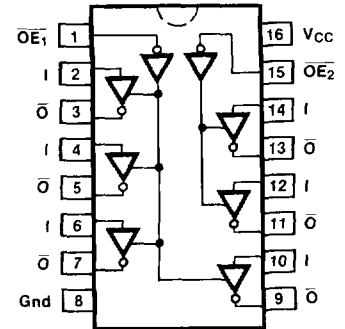
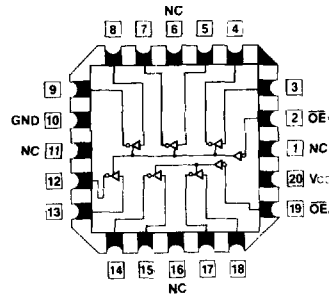
Hex Inverter Buffer
With 3-State Outputs

- 3-State Buffer Outputs Sink 64 mA
- High-Speed
- Bus-Oriented

PRELIMINARY



'F366



'F368

Pin Assignment
for LCC and PCC

Pin Assignment
for DIP and SOIC

Ordering Code: See Section 5

Input Loading/Fan-Out: See Section 3 for U.L. definitions

Pin Names	Description	54F/74F(U.L.) HIGH/LOW
$\overline{OE}_1, \overline{OE}_2$	3-State Output: Enable Input (Active LOW)	0.5/0.375
I	Inputs	0.5/0.375
O, \overline{O}	Outputs	75/40 (30)

Function Table, 'F366

Inputs			Outputs	
\overline{OE}_1	\overline{OE}_2	I	O	\overline{O}
L	L	L	L	H
L	L	H	H	L
X	H	X	Z	Z
H	X	X	Z	Z

Function Table, 'F368

Inputs		Outputs	
\overline{OE}	I	O	\overline{O}
L	L	L	H
L	H	H	L
H	X	Z	Z

L = LOW Voltage Level
H = HIGH Voltage Level
X = Immaterial
Z = High Impedance

DC Characteristics over Operating Temperature Range (unless otherwise specified)

Symbol	Parameter	54F/74F			Units	Conditions
		Min	Typ	Max		
I_{CCH} I_{CCL} I_{CCZ}	Power Supply Current		35 60 60	50 90 90	mA	$V_{CC} = \text{Max}$

AC Characteristics: See Section 3 for waveforms and load configurations

Symbol	Parameter	54F/74F		54F		74F		Units	Fig. No.
		$T_A = +25^\circ\text{C}$ $V_{CC} = +5.0\text{ V}$ $C_L = 50\text{ pF}$		$T_A, V_{CC} = \text{Mil}$ $C_L = 50\text{ pF}$		$T_A, V_{CC} = \text{Com}$ $C_L = 50\text{ pF}$			
		Min	Typ	Max	Min	Max	Min		
t_{PLH} t_{PHL}	Propagation Delay		7.0					ns	3-1 3-3
t_{PZH} t_{PZL}	Enable Time		14.0 11.0					ns	3-1, 3-13 3-12
t_{PHZ} t_{PLZ}	Disable Time		9.0 17.0					ns	3-1, 3-13 3-12